PENDING CLAIMS

This list includes all claims currently pending in the application. No claims are added, deleted, or amended in this correspondence.

1.-67. (Cancelled)

68. (Original) A monolithic 3-dimensional memory array comprising:

a first set of strips including a first terminal;

a second set of strips including a second terminal;

a third set of strips including a third terminal;

a first pillar having at least one side wall with a slightly positive slope, said pillar disposed between and connecting said first and second sets of strips, and including a first P doped silicon region, a first N doped silicon region and a first insulating region;

a second pillar having at least one side wall with a slightly positive slope, said pillar disposed between and connecting said second and third sets of strips, and including a second P doped silicon region, a second N doped silicon region and a second insulating region;

wherein each of the pillars is substantially free of stringers.

69. (Original) The memory of Claim 68, wherein each of said first and second insulating regions is disposed between the respective P and N doped silicon regions.

70. (Original) The memory of Claim 68, wherein the respective P and N doped silicon regions abut one another.

PAGE 3/11 * RCVD AT 10/29/2004 6:02:43 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/1 * DNIS:8729306 * CSID:408 969 4849 * DURATION (mm-ss):03-52